ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

SYSTEM AND METHOD OF AUTOMATICALLY GENERATING KERF DESIGN DATA

Application Number:

10/605,109

Confirmation Number:

First Named Applicant:

Howard Barrett

Attorney Docket Number:

BUR920030106US1

Art Unit:

Examiner:

MAGIN DIMYAN

Search string:

(6553559 or 6330708 or 20030009739).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1740	1	6553559	2003-04-22	Liebman et al.		716	19
m 71	2	6330708	2001-12-11	Parker et al.		716	19

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init Cite.No.	nit Cite.No. Pub. No. Date		Applicant Kind Class Subc			
M7) 1	20030009739	2003-01-09	Watanabe et al.		716	19
Signature	And	11/30/05				
	Examiner-Na	Date				
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SUPPLEMENT	CAL FORM PTO-1449	(Modified)	ATTY. DOCKET NO. BUR920030106US1	ATTY. DOCKET NO. BUR920030106US1		SERIAL NO. Unassigned 10/605, 109			
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT BUR920030106US1 Unassigned 10/605, 109 APPLICANT: Howard T. Barrett, et al.									
(Use several she	eets if necessary)	PARTE	FILING DATE: 09/09	1/03	GROUP: Unassigned	2825			
REFERENCE DESIGNATION U.S. PATENT DOCUMENTS									
EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLAS	S SUBCLAS	FILING DATE (IF APPRO.)			
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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO			
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17-10	K. Ahdoot, et al., "IBM FSD VLSI Chip Design Methodology", Proceedings of Twentieth Design Automation Conference on Design Automation, June 1983, pp. 39-45.								
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EXAMINER DATE CONSIDERED									
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